



Docket No.: 5287-0101PUS1
(PATENT)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:
Shinya SONOBE et al.

Application No.: 10/563,100

Confirmation No.: 6761

Filed: December 30, 2005

Art Unit: 2818

For: SEMICONDUCTOR ELEMENT

Examiner: Earl N. Taylor

LETTER SUBMITTING CORRECTED DRAWINGS

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Pursuant to the USPTO Notice of Allowability Form PTO-37 dated June 3, 2008 attached to the Notice of Allowance and Issue Fee Due Form PTOL-85, attached hereto is one sheet of corrected drawings.

Please substitute these corrected drawings for the corresponding one sheet of drawings on file in the above-identified application.

If necessary, the Commissioner is hereby authorized in this, concurrent, and future replies, to charge payment or credit any overpayment to Deposit Account No. 02-2448 for any additional fee required under 37 C.F.R. §§ 1.16 or 1.17; particularly, extension of time fees.

Dated:

SEP 02 2008

Respectfully submitted,

By

Andrew D. Meikle

Registration No.: 32,868

BIRCH, STEWART, KOLASCH & BIRCH, LLP

8110 Gatehouse Road

Suite 100 East

P.O. Box 747

Falls Church, Virginia 22040-0747

(703) 205-8000

Attorneys for Applicant